

Phase Advanced Dynamic Voltage Restorer Control System Design

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Abstract The increment of voltage-sensitive load equipment has made industrial processes much more susceptible to degradation in the quality of power supply. Voltage deviations, often in the form of voltage sags, can cause severe process disruptions and result in substantial economic loss. Among the several novel custom power devices, the dynamic voltage restorer (DVR) for application in distribution systems is a recent invention. The compensation capability of a DVR depends primarily on the maximum voltage injection ability and the amount of stored energy available within the restorer. In this paper a new phase advance compensation (PAC) strategy for the DVR is proposed in order to enhance the voltage restoration property of the device. Also a method of determining the exact amount of voltage injection required to systematically correct a specific voltage drop is proposed with minimum active power injection. Using the proposed method it can be shown that a particular disturbance can be corrected with less amount of storage energy compared to that of existing in-phase boosting method. Analytical expressions for both the magnitude and angle of the injected voltage are also derived. A closed-loop controller that consists of an inner current loop and an outer voltage loop is also incorporated into the DVR system. Theoretical analysis shows that the capacitor current feedback scheme is superior over the inductor current feedback scheme in terms of steady state error and disturbance rejection.

Keywords DVR, Minimal energy control, Power quality, FACTS

1. Introduction

A dynamic voltage restorer is a power quality (custom power) device used to correct the voltage disturbances by injecting voltage as well as power into the system. Voltage deviations, often in the form of voltage sags, can cause severe process disruptions and result in substantial economic loss. Therefore, cost-effective solutions, which can help such sensitive loads, ride through momentary power supply disturbances, have attracted much research attention. Among the several novel custom power devices, the dynamic voltage restorer (DVR) for application in distribution systems is a recent invention.

A series-connected converter-based mitigation device DVR [1-6] has been proposed for protecting sensitive loads from such voltage sags. The series interconnection allows the DVR injected voltage to be added to the utility supply voltage. The amplitude and phase angles of the injected three-phase voltages can be varied, thereby allowing control of real and reactive power exchanged between the DVR and the distribution system. The reactive power can be internally generated without AC passive components, while the real power exchange between the DVR and the load needs to be

supplied by external means. The amount of real power and reactive power provided by the DVR depends on the type of voltage disturbance, the power requirements of the protected load and the direction and magnitude of the injected voltage. The compensation capacity of a particular DVR depends on voltage injection ability and the maximum real power that can be supplied by the DVR. Thus the capacity of the energy storage device can become a limiting factor in compensating long duration voltage sags. If the DC-link energy storage can be further supplied by some means during compensation, the DVR can mitigate long duration voltage sags effectively [7].

A schematic diagram of the DVR incorporated into a distribution network is shown in Fig.1. In the figure, V_s is the source voltage, V_1 is the incoming supply voltage before compensation, V_{dvr} is the series injected voltage of the DVR, and I is the line current [5, 6]. The restorer typically consist of an injection transformer, the secondary winding of which connected in series with the distribution line, a voltage-sourced PWM inverter bridge connected to the primary of the injection transformer and an energy storage device connected at the dc-link of the inverter bridge. The inverter bridge output is filtered in order to mitigate the switching frequency harmonics generated in the inverter.

The injection of an appropriate V_{dvr} in the face of an up-stream voltage disturbance requires a certain amount of real and reactive power supply from the DVR. It is quite usual for the real power requirement of the DVR to be provided by the energy storage device in the form of a battery, a

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capacitor bank, or a flywheel. The inverter generates the reactive power requirement. Widely used in present DVR control is the so-called in-phase voltage injection technique where the load voltage V_2 is assumed to be in-phase with the presag voltage. As the DVR is required to inject active power into the distribution line during the period of compensation, the capacity of the energy storage unit can become a limiting factor in the disturbance compensation process[8-11]. For sags of long duration, this could result in poor load ride-through capability but it is not necessary for V_2 to be in-phase with the presag voltage[5]. Indeed, V_2 vector can lie anywhere on the periphery of a circle, the radius of which equals to the amplitude of the presag voltage (Fig.2). The disturbance correction capability of the restorer depends very much on the maximum voltage injection capability of the device and the amount of energy it can supply over the sag period. In progressive phase advance technique, all three-phase voltages are progressively advanced by a certain angle to minimize the amount of real power supplied by the DVR. Mitigation of deep sag with long duration cannot be solved with the progressive phase advance technique alone, as it is only a way of optimizing existing DVR energy storage[12]. This method is also referred as Phase Advance Compensation (PAC). Furthermore, the PAC method is to include a new closed-loop load voltage and inner-loop current mode control[13-17]. In essence, the control scheme requires the filter capacitor current to be fed back to achieve a sinusoidal capacitor current while an outer voltage loop is included to regulate the output voltage. A feed-forward loop has also been incorporated to reduce the steady-state error in the load voltage. This paper is therefore organized as follows: the general principles of the DVR operation is described in section 2; the simulation of an example for DVR operation is presented in section 3; the basic control methods of the DVR are presented in section 4 and the effectiveness of the proposed DVR control systems are evaluated, with an example presented in section 5.

2. Basic Operation of Dynamic Voltage Restorer

The corresponding phasor diagram describing the voltage sag is depicted in Fig.2, where only the affected phase is shown for clarity. With the voltage quantities as defined in section 1, let I , ϕ , δ and α represent the load current, load power factor angle, supply voltage phase angle, and load voltage advance angle, respectively. Unlike the in-phase voltage injection technique, the proposed PAC technique is realized by the adjustment in α . One distinct advantage of the proposed scheme is that less real power is needed to be injected from the DVR energy storage unit into the distribution system [2,18,19]. Assume that the load has an inductance L_l , a resistance R_l , and the DVR harmonic filter has an inductance L_f , a resistance R_f , and a capacitance C_f . The DVR injection transformer has a combined winding resistance R_t and an inductance L_t . Consider the power flow contributions

of the DVR under the PAC scheme. If P_{in} and P_{load} are the input power from the source and the load power respectively, and similarly, if Q_{in} and Q_{load} are the input reactive power from the source and load reactive power respectively, then

$$P_{dvr} = P_{in} - P_{load} = 3 \cdot V_2 \cdot I \cdot \cos(\phi) - \sum_i V_{li} \cdot I_i \cdot \cos(\phi - \alpha + \delta_i) \quad (1)$$

$$Q_{dvr} = Q_{in} - Q_{load} = 3 \cdot V_2 \cdot I \cdot \sin(\phi) - \sum_i V_{li} \cdot I_i \cdot \sin(\phi - \alpha + \delta_i) \quad (2)$$

Where $i=1, 2, 3$. For minimum power operation and given ϕ , δ , V_l , and V_2 we have

$$\frac{dP_{dvr}}{d\alpha} = 0 \quad (3)$$

Then

$$\sum_i V_{li} \cdot I_i \cdot \sin(\phi - \alpha + \delta_i) = 0 \quad (4)$$

$$\text{If } P_{dvr} > 0, \text{ then } \alpha_{opt} = \phi + \beta \quad (5)$$

Where

$$\beta = \arctan \frac{Y}{X}, \quad X = \sum_i V_{li} \cdot \cos(\delta_i),$$

$$Y = \sum_i V_{li} \cdot \sin(\delta_i)$$

Else if $P_{dvr} = 0$, then

$$\alpha_{opt} = \phi + \beta - \arccos\left(\frac{3 \cdot V_2 \cdot \cos(\phi)}{\sqrt{X^2 + Y^2}}\right) \quad (6)$$

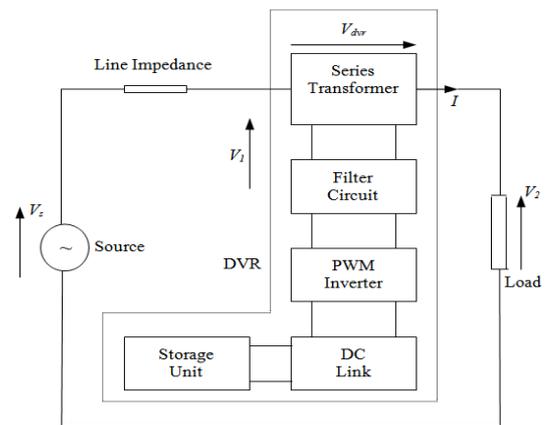


Figure 1. Schematic of a power distribution system compensated by a DVR

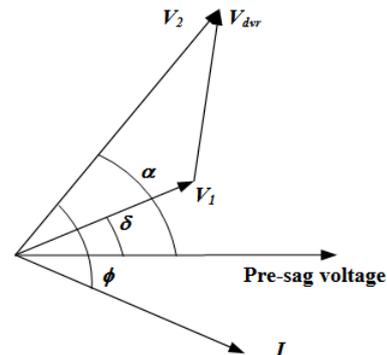


Figure 2. Phasor diagram of power distribution system during a sag

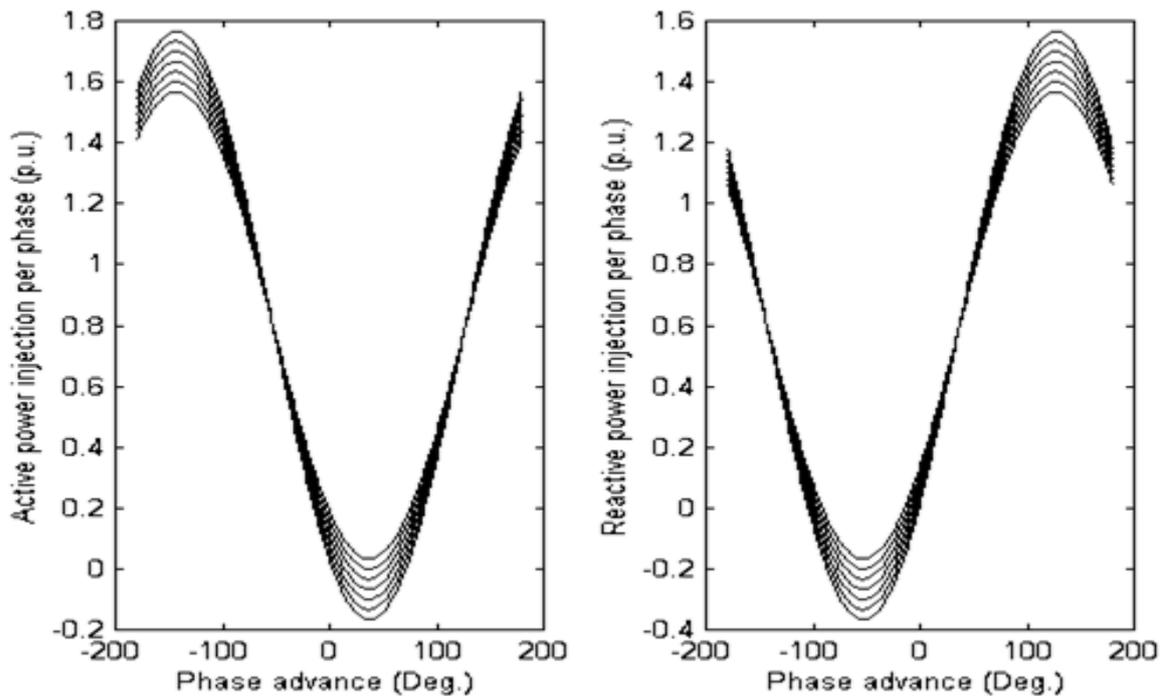
3. Comparison of In-Phase Injection with PAC

In this section single-phase, two-phase, and three-phase voltage sags are simulated with mentioned power injection equations. Consider the typical system arrangement as shown in Fig.1 where the sensitive load is assumed to have a power factor of 0.8 lag and the presag load voltage and current are at 1 p.u. The voltage sags for three cases are 10-70%. Fig.3. shows the relationship between the real power supplied by the DVR and the corresponding phase advance angle α . In case (a), it can be seen that the power required for in-phase injection ($\alpha = 0$) and 30% sag is 0.08 p.u. and this decreases with the advance in α . Negative DVR real power means that the DVR absorbs the power from the line which is undesirable. In case (a) for 30% sag the DVR required 0.06 p.u. reactive power for in-phase injection and this increases with the α . For cases (b) and (c) it can be seen that the DVR can control the load voltage without supplying any real power to the load for a balanced sag level of up to 0.2 p.u. Based on the above analysis, the amount of storage energy can be reduced, thus resulting in a more economical restorer in terms of a more compact design and it is possible to ride through some sags without the need to supply energy from the DVR. Therefore, the sag duration is not an important issue for this type of sag, while it can be extremely critical for the in-phase boosting technique. But with PAC, magni-

tude of the injected voltage is larger than that of based on in-phase injection method and as the phase advancement angle needs to be determined in real time, the control system is more complex.

4. DVR Inverter Modeling and Control System Design

In the presence of semiconductor switches in the inverter bridge, the DVR inverter model is non-linear. However it is possible to describe the DVR behaviour in differential or difference form using the state space averaging technique. The dynamic characteristics of the inverter are mainly governed by the LC filter and connected load[4, 9, 10]. The connected load can either be linear static, non-linear, dynamic or their combination. Modeling of a non-linear load may require a different approach. In this study the load is assumed to be linear RL type with an added disturbance to represent load dynamics. By modeling the load in this manner, it is possible to observe the controller behaviour due to the dynamic changes in the load. The equivalent single line diagram of the DVR inverter is shown in Fig.4 where negligible line impedance between the DVR and the supply voltage bus bar (point of common coupling) is assumed. Using this diagram the following expression can be derived in state space form.



(a)

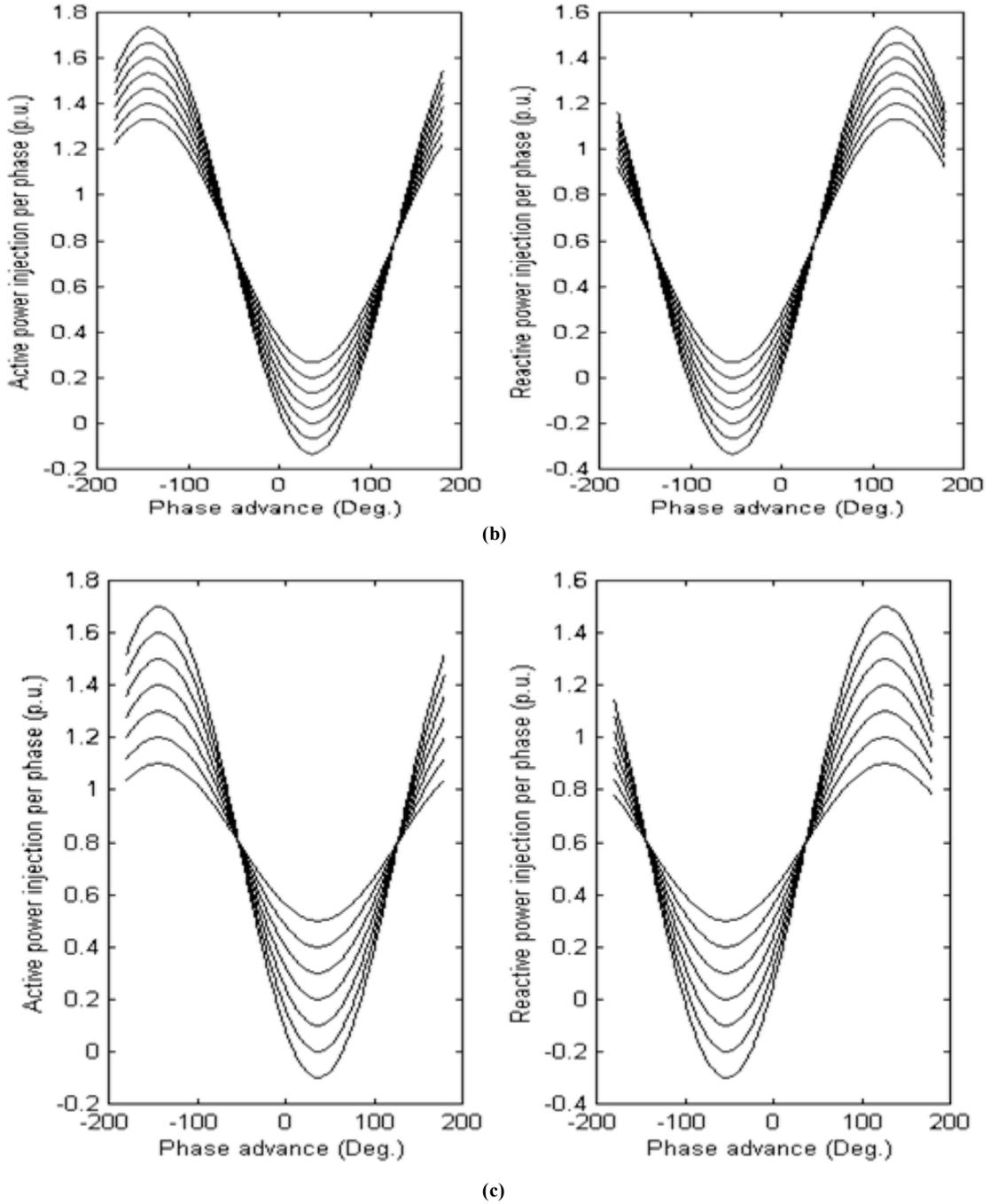


Figure 3. Active and reactive power injected from DVR versus phase advance α for: (a) single phase voltage sags; (b) two phase voltage sags; (c) three phase voltage sags

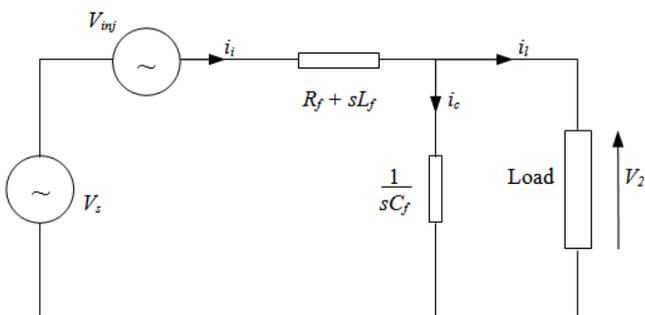


Figure 4. Equivalent circuit of the DVR inverter

$$\begin{bmatrix} \frac{di_i}{dt} \\ \frac{dV_2}{dt} \end{bmatrix} = \begin{bmatrix} -\frac{R_f}{L_f} & -\frac{1}{L_f} \\ \frac{1}{C_f} & 0 \end{bmatrix} \begin{bmatrix} i_i \\ V_2 \end{bmatrix} + \begin{bmatrix} \frac{1}{L_f} & 0 \\ 0 & -\frac{1}{C_f} \end{bmatrix} \begin{bmatrix} V_s \\ i_l \end{bmatrix} + \begin{bmatrix} \frac{1}{L_f} \\ 0 \end{bmatrix} [V_{inj}] \quad (7)$$

Where i_i is filter inductor current, i_l is load current, and V_{inj} is the injected voltage. Fig.5 shows the open loop controller block diagram of the DVR inverter. The transfer functions of blocks $G_f(s)$, $G_c(s)$ and $G_l(s)$ are given by the following expressions

$$G_f(s) = \frac{1}{(R_f + s \cdot L_f)}, \quad G_l(s) = \frac{1}{(R_l + s \cdot L_l)}, \quad G_c(s) = \frac{1}{s \cdot C_f}$$

The transfer function between load voltage and input reference is expressed as follows

$$G_{open}(s) = \frac{V_2(s)}{V_{ref}(s)} = \frac{k_i \cdot (R_l + L_l \cdot s)}{a \cdot s^3 + b \cdot s^2 + c \cdot s + d} \quad (8)$$

Where

$$a = L_f \cdot L_l \cdot C_f, \quad b = C_f \cdot (R_l \cdot L_f + L_l \cdot R_f)$$

$$c = L_l + L_f + C_f \cdot R_l \cdot R_f \quad \text{And} \quad d = R_f + R_l$$

Using Routh-Hurwitz stability criteria it can be shown that the system is inherently stable for all possible values of system parameters. One root is a real root and is approximately located at $-R_l/L_l$ and real parts of other two dominant complex roots are given by $-R_f/2L_f$. Therefore the damping of the system is governed by filter resistance R_f . Increasing the filter resistance to improve damping and to control overshoot at the instant that DVR begins its operation may lead to other problems such as increasing power loss, introduction of a phase angle between the inverter and filter outputs and filter voltage drop[20,21]. Therefore an increasing in the filter resistance may not be beneficial in view of system efficiency and accuracy. One of the essential requirements of the DVR controller is its ability to correct the load voltage towards its desired level. Required injected voltage is achieved by appropriately controlling the inverter output voltage[22, 23]. The inner current loop can be formed using filter inductor current, filter capacitor current or combination of the two currents. The following sections present those controller options with a comparative analysis of their merits and limitations.

4.1. Closed-Loop Controller with Filter Inductor Current Feedback

In this control option, inner loop is formed using filter inductor current and there is also a feed forward loop consisting of difference between the reference voltage and the input supply voltage. This loop intends to enhance dynamic performance in terms of response time and steady state error. The controller block diagram is shown in Fig.6. Transfer function is

$$G_{close}(s) = \frac{V_2(s)}{V_{ref}(s)} = \frac{(1 + k_v \cdot k_c) \cdot k_i \cdot (R_l + L_l \cdot s)}{a \cdot s^3 + b \cdot s^2 + c \cdot s + d} \quad (9)$$

Where

$$a = L_f \cdot L_l \cdot C_f,$$

$$b = C_f \cdot (R_l \cdot L_f + L_l \cdot R_f + k_c \cdot k_i \cdot L_l)$$

$$c = L_l + L_f + C_f \cdot R_l \cdot R_f + k_c \cdot k_v \cdot k_i \cdot L_l + k_c \cdot k_i \cdot C_f \cdot R_l$$

$$d = R_l + R_f + k_c \cdot k_v \cdot k_i \cdot R_l + k_c \cdot k_i$$

4.2. Controller with Capacitor Current Feedback

The block diagram of this control system is similar to that of shown in Fig.6 except that the current feedback is taken from the capacitor current i_c . The transfer function between load voltage and input voltage reference is

$$G_{close}(s) = \frac{V_2(s)}{V_{ref}(s)} = \frac{(1 + k_v \cdot k_c) \cdot k_i \cdot (R_l + L_l \cdot s)}{a \cdot s^3 + b \cdot s^2 + c \cdot s + d} \quad (10)$$

Where a, b , and c are the same in last section, but d is

$$d = R_l + R_f + k_c \cdot k_v \cdot k_i \cdot R_l$$

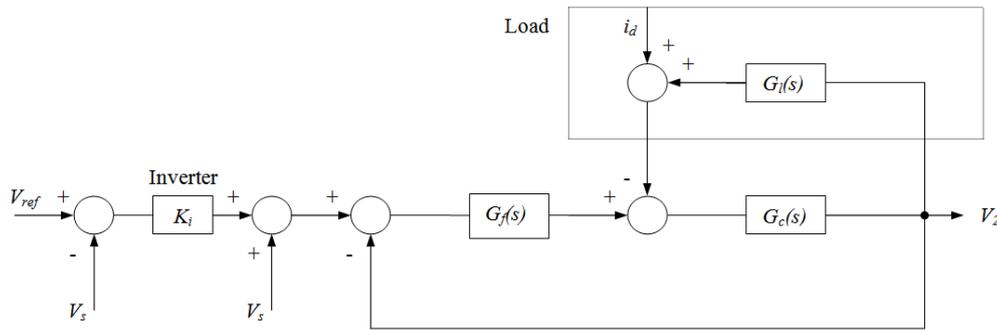


Figure 5. Open loop controller block diagram of the DVR inverter

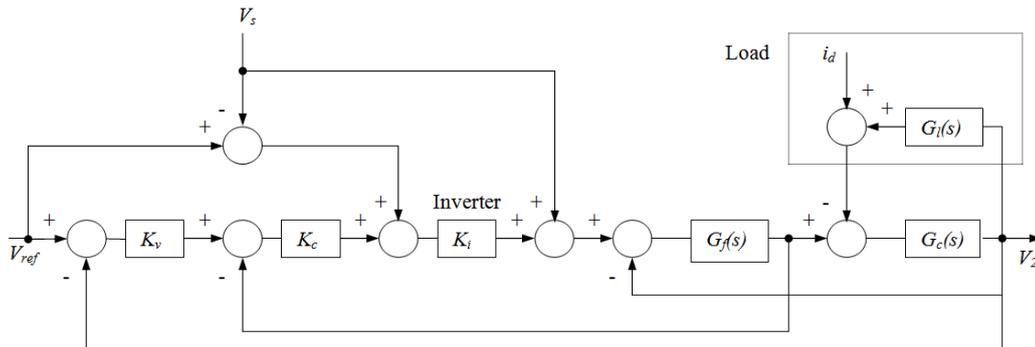


Figure 6. Closed loop control system with inductor current feedback

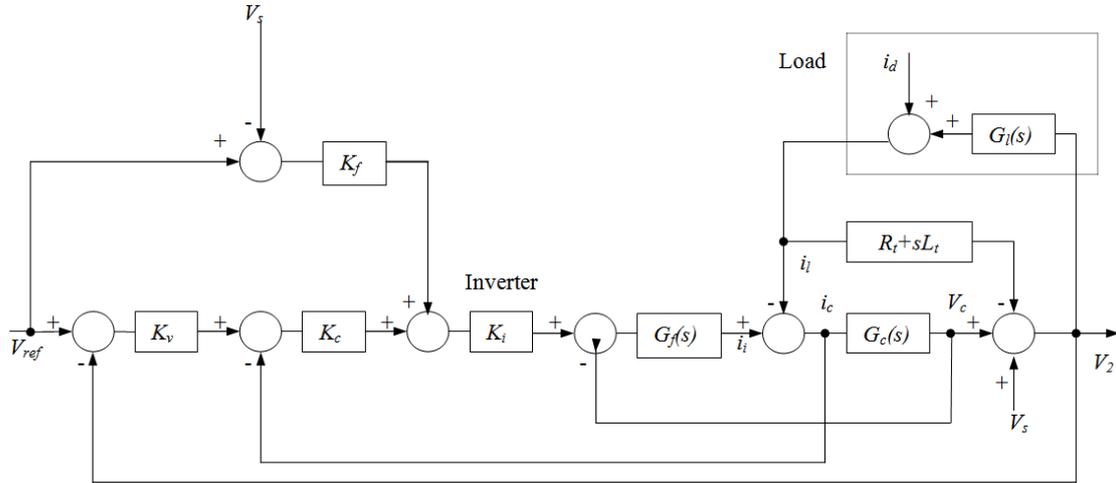


Figure 7. Multi-loop feedback controller block diagram for voltage regulation

4.3. Closed-Loop Controller with Combination of Capacitor and Inductor Current Feedback

As the load current has direct relationship with the inductor current, if a portion of the inductor current is added to the capacitor current feedback, the system performance can be further improved. This can be verified by deriving the transfer functions between the load voltage and input voltage reference. The transfer function is

$$G_{close}(s) = \frac{V_2(s)}{V_{ref}(s)} = \frac{(1 + k_v \cdot k_c \cdot k_f) \cdot k_i \cdot (R_l + L_l \cdot s)}{a \cdot s^3 + b \cdot s^2 + c \cdot s + d} \quad (11)$$

Where

$$a = L_f \cdot L_l \cdot C_f$$

$$b = C_f \cdot (R_l \cdot L_f + L_l \cdot R_f + k_f \cdot k_i \cdot L_l + k_c \cdot k_f \cdot k_i \cdot L_l)$$

$$c = L_l + L_f + C_f \cdot R_l \cdot R_f + k_f \cdot k_i \cdot R_l \cdot C_f + k_c \cdot k_f \cdot k_i \cdot R_l \cdot C_f + k_v \cdot k_c \cdot k_f \cdot k_i \cdot L_l$$

$$d = R_l + R_f + k_f \cdot k_c \cdot k_v \cdot k_i \cdot R_l + k_f \cdot k_i$$

5. Simulation Results

A detailed simulation of the proposed DVR control system has been carried out using MATLAB software. The system parameters are given in the Appendix.

5.1. Controller with Inductor Current Feedback

For a step input, the steady state error between the reference voltage and the load voltage can be expressed as

$$SSE = \left(1 - \frac{(1 + k_v \cdot k_c) \cdot k_i \cdot R_l}{R_l + R_f + k_c \cdot k_v \cdot k_i \cdot R_l + k_c \cdot k_i}\right) \cdot 100 = 26.57\% \quad (12)$$

This system suffers from large steady state error which cannot be compensated even with the incorporation of feed forward loop. It is clear from Fig.8 that the overshoot is large in the load voltage. Furthermore bode diagram shown in Fig.9 reveals that the low frequency disturbances will affect load voltage performance significantly as the transfer function has a higher gain in this region. Hence the inductor

current feedback system is found not suitable for the proposed DVR system.

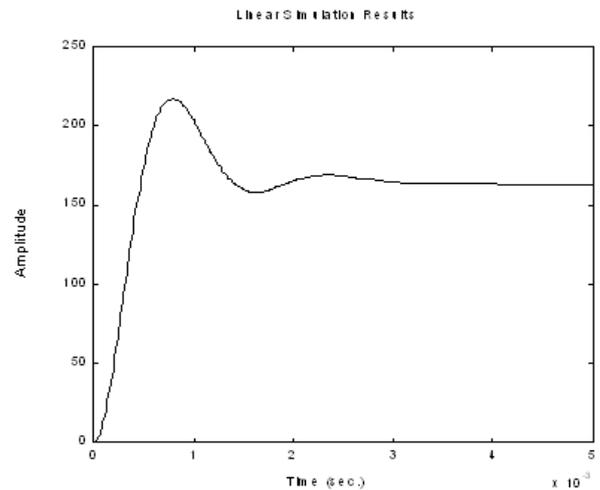


Figure 8. Step response of controller with inductor current feedback

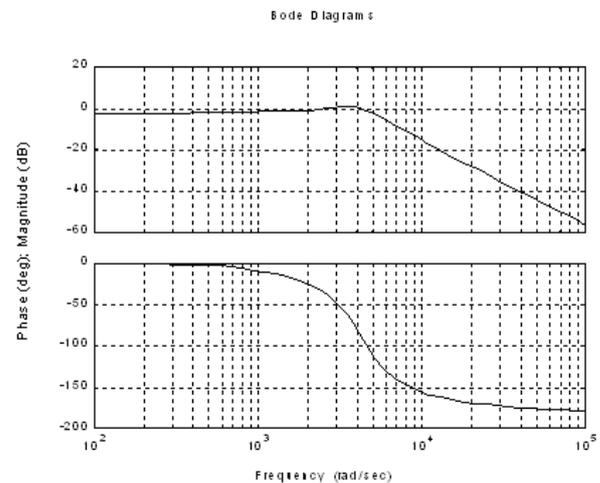


Figure 9. Bode diagram of controller with inductor current feedback

5.2. Controller with Capacitor Current Feedback

For a step input, the steady state error between the reference voltage and the load voltage is

$$SSE = \left(1 - \frac{(1 + k_v \cdot k_c) \cdot k_i \cdot R_l}{R_l + R_f + k_c \cdot k_v \cdot k_i \cdot R_l}\right) \cdot 100 = 18.39\% \quad (13)$$

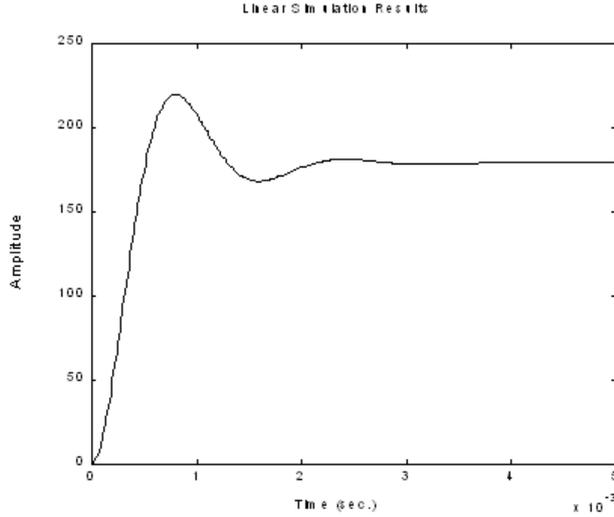


Figure 10. Step response of controller with capacitor current feedback

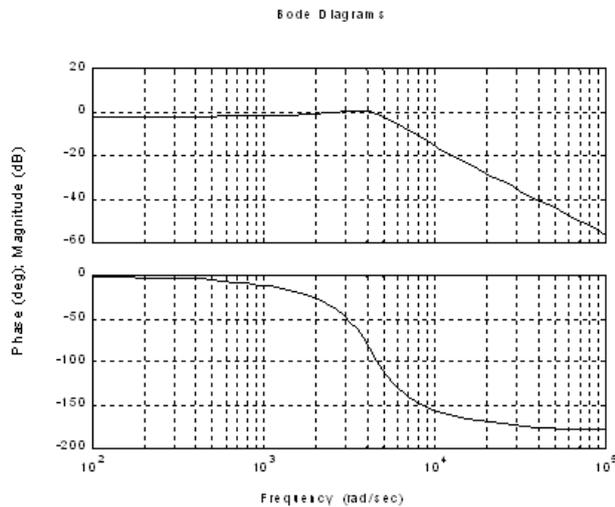


Figure 11. Bode diagram of controller with capacitor current feedback

On the contrary, the steady state error given in this control method can be made small by proper adjustment of k_v and k_c while maintaining a good system damping. Fig. 10 shows the step response for this system. Bode diagram in Fig. 11 reveals that the system can attenuate both high and low frequency disturbances except those in the frequency range of 200Hz–2 kHz. This shows that the controller is not affected by dynamic changes of the load at the operating frequency. Furthermore this controller can respond quickly for any changes in the load voltage, as the capacitor current is directly proportional to the rate of change of load voltage. Hence the capacitor current feedback system is superior over the inductor current feedback in terms of dynamic and steady state performance.

5.3. Controller with Combination of Capacitor and

Inductor Current Feedback

For a step input, the steady state error can be expressed as

$$SSE = \left(1 - \frac{(1 + k_v \cdot k_c \cdot k_f) \cdot k_i \cdot R_l}{R_l + R_f + k_f \cdot k_c \cdot k_v \cdot k_i \cdot R_l + k_f \cdot k_i}\right) \cdot 100 = 4.84\% \quad (14)$$

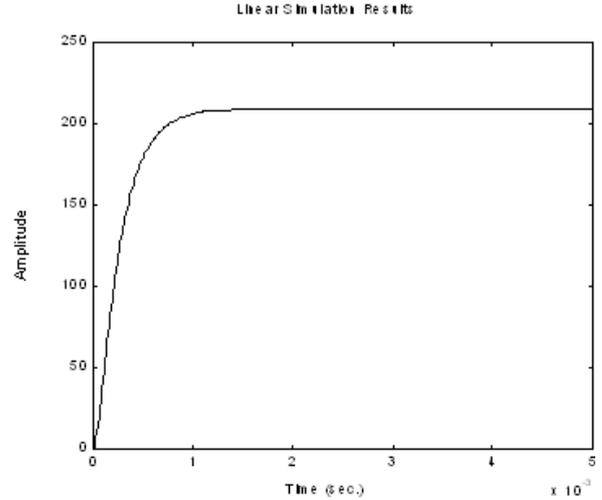


Figure 12. Step response of controller with combination of capacitor and inductor current feedback

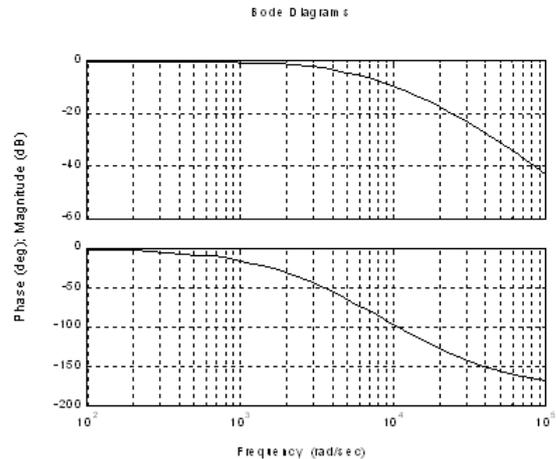


Figure 13. Bode diagram of controller with combination of capacitor and inductor current feedback

It has been shown that the capacitor current feedback system gives better dynamic performance, damping and robustness over high and low frequency ranges of the load disturbance. But the load disturbance in the frequency range of 200 Hz to about 2 kHz will have a direct effect on the load voltage. Fifth and seventh harmonics components of load current fall into this range[24]. As the load current has direct relationship with the inductor current, if a portion of the inductor current is added to the capacitor current feedback, the system performance can be further improved. The bode diagram shown in Fig. 13 reveals that it will attenuate load disturbance over the entire frequency range effectively. This means that the controller is more robust against dynamic changes in the load compared to that of controllers discussed previously.

6. Conclusions

A new phase advanced multi-loop control scheme has been proposed for the dynamic voltage restorer. Through analysis simulation, it is shown that the proposed scheme is superior compared to the conventional in-phase injection technique in terms of energy saving and dynamic performance. Such characteristics are highly desirable as the design is seen to lead in a more economical restorer which can improve the ride-through capability of sensitive loads and industrial processes. The analysis has shown that the DVR has poor dynamic performance and low stability margin when it is operated with the open loop controller. The closed-loop control scheme with an inner current loop and outer voltage loop has increased the quality of load voltage by improving the damping, stability and the dynamic performance. Theoretical analysis has proven that the capacitor current feedback scheme is superior over the inductor current feedback scheme in terms of steady state error and disturbance rejection. Controller robustness to load variations can further be improved by taking a combination of inductor and capacitor current feedback loops into the inner current loop.

Appendix

System parameters are :

Supply voltage(rms) : 220 V

Filter inductance : 5 mH

Filter resistance : 0.4 Ω

Filter capacitance : 30 μ F

Load inductance : 114 mH

Load resistance : 57 Ω

$k_v = 0.1$, $k_c = 35$, $k_i = 0.5$, $k_f = 6$

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